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**August 2, 2005**

**AMENDMENT AFTER FINAL**

Atty Docket No. :	JCLA12969
Appl. No. :	10/786,703
Filing Date :	February, 24, 2004
Pages :	Cover + 8

**BY FACSIMILE ONLY**

Fax No. :	571-273-8300
Attention :	EXAMINER : QUINTO, KEVIN V.
Group Unit :	2826
From :	Jiawei Huang, Reg. No. 43,330
MESSAGE :	Enclosed herewith is an Amendment in 8 pages.

Sir:

I hereby certify that this correspondence is being facsimile transmitted to the Patent and Trademark Office on **August 2, 2005** at the above indicated fax number.

Sign by:

  
Michelle Chang

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**Application No.: 10/786,703**

**AUG 02 2005**

Docket No.: JCLA12969

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**In Re Application of :**

Chih-Feng Huang et al.

Serial No. : 10/786,703

Filed : 02/24/2004

For : HIGH VOLTAGE LDMOS  
TRANSISTOR HAVING AN  
ISOLATED STRUCTURE

Examiner : QUINTO, KEVIN V

Art Unit : 2826

Docket No. : JCLA 12969

**AMENDMENT AFTER FINAL**

**MAIL STOP AF**  
Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir,

The Office Action dated May 19, 2005 has been carefully considered. In response thereto, please enter the following amendments and consider the following remarks.